Tachyum Prodigy® TPU™ IoT/Edge Licensable Hardware IP

Tachyum’s Prodigy® is the first Universal Processor combining General Purpose Processors, High Performance Computing (HPC), Artificial Intelligence (AI), Deep Machine Learning, Explainable AI, Bio AI, and other AI disciplines with a single chip. With the tremendous growth of the AI chipset market for edge inference, Tachyum TPU™ (Tachyum Processing Unit) is positioned to expand the unique value proposition of its Tachyum Prodigy® based training to its TPU™ AI Inference Engine, as a licensable core. That will allow IoT and Edge developers take full advantage of Tachyum Prodigy® datacenter trained AI to make IoT/Edge devices intelligent through TPU™ IP.

**Specifications**
- TPU AI/ML Inference IP Architecture

**Simulators**
- Architectural transactional simulator
- Cycle accurate simulator

**Tools Support**
- Assembler
- Linker
- Debugger
- Loader

**Compilers Support**
- C/C++ compiler
- Fortran compiler
- TensorFlow
- PyTorch

**Operating System**
- Linux

**Hardware Licensable IP**
- RTL in Verilog
- UVM Testbench
- Synthesis constraints